

Position information – Full professor (military), position no. 2

University	MILITARY TECHNICAL ACADEMY „ FERDINAND I “				
Faculty	Faculty of Communications and Electronic Systems for Defense and Security				
Department	Department of Military Electronic Equipment and Systems				
Position no.	2				
Position name	Full professor (military)				
Topics	<ul style="list-style-type: none"> - Digital circuits - Embedded systems II - Digital image processing in electronic systems for surveillance and recognition - Artificial intelligence in electronic systems for defense and security 				
Scientific field	Electronics, communications and Information Technology Engineering				
Position description	Full professor (military), position no.2, Department of Military Electronic Equipment and Systems / Faculty of Communications and Electronic Systems for Defense and Security, including lectures and research activities, according to topics included in the educational plans contained in the job.				
Responsibilities/specific activities	Carries out didactic and scientific activities based on the established didactic norm; conducts scientific research activities; participates in the activities of elaborating education-specific documents from the didactic commission of which he is a member; gives consultations to students/trainees; contributes to the provision of bibliographic material for the study subjects, through the development of manuals, books, scientific works; examines and assesses with marks the preparation of students and trainees, permanently and in exam sessions; participates in curriculum design in his field of competence; carry out extracurricular activities.				
Minimum employment wage					
Deadlines					
Date of publication of the announcement in “Monitorul Oficial”	30.04.2024 (Monitorul Oficial al României partea a III-a, nr. 143/30.04.2024 PUBLICARE CONCURS POSTURI DIDACTICE)				
Enrollment period	<table border="1"> <tr> <td>Start</td> <td>End</td> </tr> <tr> <td>30.04.2024</td> <td>02.08.2024</td> </tr> </table>	Start	End	30.04.2024	02.08.2024
Start	End				
30.04.2024	02.08.2024				
Lecture date	11.09.2024				
Lecture hour	11.30				

Lecture place	Military Technical Academy „Ferdinand I“, Building U, Room U1-05	
Exam period	Start 02.09.2024	End 11.09.2024
Results period	Start 12.09.2024	End 12.09.2024
Appeals period	Start 13.09.2024	End 17.09.2024
Topics	<p>Topics:</p> <ul style="list-style-type: none"> - Digital concepts, number systems, operations, and codes; logic gates; Boolean algebra and logic simplification; combinational logic analysis; latches, flip-flops, and timers; shift registers; counters; data storage; signal conversion and processing; integrated circuit technologies; - MCU oscillators; MCU architecture; CPU and ALU; MCU memory organization; interrupts; I/O ports; timers and counters; CCP modules; serial communications; voltage references; analog comparator; A/D converters; ICSP; - Digital image fundamentals; intensity transformations and spatial filtering; filtering in the frequency domain; image restoration and reconstruction; color image processing; wavelets and multiresolution processing; - classifiers based on Bayes decision; linear and nonlinear classifiers; feature selection; feature generation; template matching; clustering; neural networks. <p>Recommended references:</p> <p>[1] Thomas L. Floyd, Digital Fundamentals, 11th Edition, Pearson, 2014;</p> <p>[2] Rafael Gonzalez and Richard Woods, Digital Image Processing, 3rd Edition, Pearson, 2022;</p> <p>[3] * * *, PICmicro Mid-Range MCU Family Reference Manual, Microchip Technology Inc., 1997;</p> <p>[4] Konstantinos Koutroumbas and Sergios Theodoridis, Pattern Recognition, 4th Edition, Academic Press, 2008.</p>	
Tender description	<p>According to HG1339/2023 and the Competition Methodology for filling vacant teaching and research positions A3283/12.04.2024, validated by the Senate of the Military Technical Academy "Ferdinand I" on 29.02.2024.</p> <p>Analysis of the file, public lecture in the presence of the committee and presentation of the university career development plan, both from a didactic point of view and from the point of view of scientific research activities.</p>	
Required documents	<p>According to HG1339/2023 and the Competition Methodology for filling vacant teaching and research positions A3283/12.04.2024, validated by the Senate of the Military Technical Academy "Ferdinand I" on 29.02.2024.</p>	

Correspondence address	Academia Tehnică Militară „Ferdinand I”, Bulevardul George Coșbuc nr. 39-49, Sector 5, BUCUREȘTI, Cod poștal 050141
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Commission:

Chairman	Prof. univ. dr. ing. Ioan NICOLAESCU, Academia Tehnică Militară „Ferdinand I”;
Members	Prof. univ. dr. ing. Simona Viorica HALUNGA, Universitatea Națională de Știință și Tehnologie POLITEHNICA, București; Prof. univ. dr. ing. Constantin PALEOLOGU, Universitatea Națională de Știință și Tehnologie POLITEHNICA, București; Prof. univ. dr. ing. Ciprian RACUCIU, Universitatea Titu Maiorescu, București; Prof. univ. dr. ing. Cazacu EMIL, Universitatea Națională de Știință și Tehnologie POLITEHNICA, București.

Appeals Resolution Commission:

Chairman:

Prof. univ. dr. ing. Ștefan SIMION, Academia Tehnică Militară „Ferdinand I”;

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