

## Dana-Mihaela VÎLCU

### Lista de lucrări

#### a) Lucrări prezentate la conferințe

1. **D. Vîlcu**, *Learning versus eLearning for IT professionals*, Proc. 13th Intl. Sc. Conf. "eLearning and Software for Education" Bucharest, April 27-28, 2017, Carol I NDU Publishing House, ISSN 2066-026X, pp. 340-34; **ISI Web Of Science Proceedings, ProQuest**
2. **D. Vîlcu**, *eLearning and mobile apps in palliative care*, Proc. 12th Intl. Sc. Conf. "eLearning and Software for Education", Bucharest, April 21-22, 2016, Carol I NDU Publishing House, DOI 10.12753/2066-026X-16-057, pp. 400-404; **ISI Web Of Science Proceedings, CEEOL**
3. **D. Vîlcu**, E. Sabău, G. Niculescu, *ICT for sport professionals. From university curricula to sport training and back*, Quality and efficiency in e-learning, Proc. 11th Intl. Sc. Conf. "eLearning and Software for Education" Bucharest, April 23-24, 2015, Carol I NDU Publishing House, 10.12753/2066-026X-15-239, pp. 386-390; **ISI Web Of Science Proceedings, CEEOL**
4. **D. Vîlcu**, E. Sabău, G. Niculescu, *E-training mobile devices and applications for sport professionals, Let's build the future through learning innovation*, Proc. 10th Intl. Sc. Conf. "eLearning and Software for Education" Bucharest, April 24-25, 2014, Ed. Universității Naționale de Apărare "Carol I", pp. 248-253; **ISI Web Of Science Proceedings**
5. E. Sabău, G. Niculescu, **D. Vîlcu**, *Virtual Environment Used in the Management of Children Obesity Through Physical Exercises*, Quality and efficiency in e-learning, Proc. 9th Intl. Sc. Conf. "eLearning and Software for Education" Bucharest, April 25-26, 2013, Vol. 3, Ed. Universității Naționale de Apărare "Carol I" (ISSN: 2066 - 026X print, 2066 - 8821 online); **ISI Web Of Science Proceedings, EBSCO, CEEOL**
6. **D. Vîlcu**, *Real Time Scheduling and CPU Power Consumption in Embedded Systems*, Miclea, L. et al. (eds.), Proc. 2008 IEEE Intl. Conf. on Automation, Quality and Testing, Robotics (AQTR 2008), 22-25 may 2008, Cluj-Napoca, Romania, Vol. I, 261-266 (2008), IEEE Catalog Number CFP08AQTR-PRT, ISBN 978-1-4244-2576-1; <http://ieeexplore.ieee.org/>; **IEEE Xplore - 10.1109/AQTR.2008.4588688**
7. **D. Vîlcu**, *Power Aware Scheduling on Variable Speed Processor*, The 8th Intl. Conf. on Informatics in Economy "Informatics in Knowledge Society", 17-18 may 2007, Bucharest, Romania, Ed. Economică, 1001-1006 (2007), ISBN 978-973-594-921-1
8. **D. Vîlcu**, *Power Aware Scheduling on Variable Speed Processor: Limits of the Theoretical Model*, Păltineanu, G. et al. (eds.), Trends and challenges in applied mathematics. Conference proceedings of the international conference ICTCAM 2007, Bucharest, Romania, June 20-23, 2007, Matrix Rom. 361-365 (2007), ISBN 978-973-755-283-9; **Zentralblatt - Zbl 1173.68417**
9. **D. Vîlcu**, G. Mercier, *A New Approach to Minimize the Energy Consumption of Hard Real Time Embedded Systems*, Radu, M. (ed. coord.), International Conference „Communications 2002”: Bucharest, 05-07 December 2002: proceedings, Ed. Academiei Tehnice Militare, 47-52 (2002), ISBN 973-8290-67-8
10. G. Mercier, **D. Vîlcu**, S. Chebira, *Real Time Wireless Transmission of Medical Images Using Predictive Control Based on CMAC Neural Networks*, Radu, M. (ed. coord.), „International Conference „Communications 2002”: Bucharest, 05-07 December 2002: proceedings”, Ed. Academiei Tehnice Militare, 117-122 (2002), ISBN 973-8290-67-8

11. G. Mercier, **D. Vîlcu**, L. George, *Real-Time Application on a CMAC Neural Network*, Dagli, C. et al. (eds.), „Intelligent Engineering Systems Through Artificial Neural Networks: Volume 11 - Smart Engineering System Design”, Proceedings of Artificial Neural Networks in Engineering Conference (ANNIE01), November 4-7, 2001, St. Louis, Missouri, ASME Press, 477-484 (2001), ISBN 0791801764; **CORE**

**b) Teza de doctorat**

*Systèmes temps réel embarqués: ordonnancement optimal de tâches pour la consommation énergétique du processeur*, 2004, Univ. Paris XII Val-de-Marne, Faculté de Sciences et Technologies, France,

[https://athena.u-pec.fr/view/UniversalViewer/33BUCRET\\_INST/1273069180004611](https://athena.u-pec.fr/view/UniversalViewer/33BUCRET_INST/1273069180004611)

**c) Cărți și capitole în cărți**

- *Structuri de date*, capitol în: G. Albeanu (coord.), N. M. Iacob, L. C. Defta, **D. M. Vîlcu**, A. Averian, „Capitole fundamentale de informatică”, Ed. Fundației România de Măine, ISBN 978-606-20-0304-3, 2018

**d) Articole în reviste**

1. M. M. Mierlă, **D. M. Vîlcu**, *Scheduling Algorithms for Project Management: a Real-Time Operating System Type Approach*, Journal of Military Technology, Vol. 5, Issue 2, 2022, ISSN 2601-6613; EBSCO, Google Scholar
2. A. L. Ciobanu, **D. M. Vîlcu**, *Trajectory analysis in the Aikido technique Shiho Nage*, Journal of Military Technology, Vol. 4, Issue 2, 2021, ISSN 2601-6613; EBSCO, Google Scholar
3. G. Mercier, **D. Vîlcu**, *Multiprocessor Scheduling Theory. A Short Survey*, Analele Universității Spiru Haret, Seria Matematică-Informatică, Anul V, Nr. 5, 2009, 63-73, ISSN 1841-7883
4. **D. Vîlcu**, *An Overview on Real Time Scheduling*, Revista Română de Automatică, Vol. XXII, Nr. 1, 2009, 56-64, ISSN 1454-9077
5. **D. Vîlcu**, *Neural Network RLS Implementation*, Analele Universității București, Secțiunea Informatică, XLVII 1/1998, 3–12

**e) Citări:**

- **D. Vîlcu**, *Real Time Scheduling and CPU Power Consumption in Embedded Systems*, Miclea, L. et al. (eds.), “Proc. 2008 IEEE Intl. Conf. on Automation, Quality and Testing, Robotics (AQTR 2008), 22-25 may 2008, Cluj-Napoca, Romania”, Vol. I, 261-266 (2008), IEEE Catalog Number CFP08AQTPRT, ISBN 978-1-4244-2576-1;

<http://ieeexplore.ieee.org/>; IEEE Xplore - 10.1109/AQTR.2008.4588688, *ISI Proceedings*

1. Y-H. Fan, *Energy-aware path strategy for embedded network*, International Journal of Distributed Sensor Networks (2017), Vol. 13, Issue 10, pp. 1-11, DOI: 10.1177/1550147717736175; *ISI Web Of Science*
2. P. M. Hettiarachchi, *A Control-Theoretic Design And Analysis Framework For Resilient Hard Real-Time Systems* (2015). Wayne State University Dissertations. Paper 1339, [http://digitalcommons.wayne.edu/cgi/viewcontent.cgi?article=2338&context=oa\\_dissertations](http://digitalcommons.wayne.edu/cgi/viewcontent.cgi?article=2338&context=oa_dissertations)
3. Al-T. Mutaz, *On the Design of Time-Predictable Low-Leakage Cache Memory for Real-Time Embedded Systems*, Computer Engineering Department, Mu'tah University, P.O. Box 7,

- Mu'tah 61710 JORDAN, WSEAS TRANSACTIONS on INFORMATION SCIENCE and APPLICATIONS, Volume 12, 2015, E-ISSN: 2224-3402,  
<http://www.wseas.org/multimedia/journals/information/2015/a185709-602.pdf>
4. Y-H. Fan, J-O. Wu, and S-F. Wang, *Energy Efficiency of Task Allocation for Embedded JPEG Systems*, The Scientific World Journal, 2014, ID. 718348, DOI: 10.1155/2014/718348,  
<https://www.ncbi.nlm.nih.gov/pmc/articles/PMC4005155/>
  5. Y-H. Fan, J-O. Wu, S-F. Wang, *Low Power Dissipation Model Analysis for Embedded Systems*,  
<http://www.globalcis.org/rnis/ppl/RNIS202PPL.pdf>
- **D. Vilcu**, E. Sabău, G. Niculescu, *ICT for sport professionals. From university curricula to sport training and back, Quality and efficiency in e-learning*, Proc. 11th Intl. Sc. Conf. "eLearning and Software for Education" Bucharest, April 23-24, 2015, Carol I NDU Publishing House, 10.12753/2066-026X-15-239, pp. 386-390,  
<http://proceedings.elseconference.eu/>; ISI Web Of Science Proceedings
1. D. Dukic, G. Kozina, B. Bodrazic, *Perceived Importance and Barriers to ICT Integation into Sport Management*, TEM Journal – Technology Education Management Informatics, Vol. 11, Issue 3, pp. 1213-1222, Aug. 2022; ISI Web Of Science
- **D. Vilcu**, E. Sabău, G. Niculescu, *E-training mobile devices and applications for sport professionals*, Proc. 10th Intl. Sc. Conf. "eLearning and Software for Education" Bucharest, April 24-25, 2014, Ed. Universității Naționale de Apărare "Carol I", pp. 248-253, <http://proceedings.elseconference.eu/>;  
*ISI Web Of Science Proceedings*
1. M. Stoicescu, M. Stănescu, *Using mobile phone technology to enhance physical education efficiency*, Proc. 11th Intl. Sc. Conf. "eLearning and Software for Education" Bucharest, April 23-24, 2015, Carol I NDU Publishing House, 10.12753/2066-026X-15-239, pp. 380-385.
- **D. Vilcu**, *Real time embedded systems. CPU power aware task scheduling*, 2004, Univ. Paris XII Val-de-Marne, Faculty of Science and Technologies, France, <http://doxa.u-pec.fr/theses/th0211370.pdf>
1. J. Francomme, *Propositions pour un protocole déterministe de contrôle d'accès et de routage avec économie d'énergie dans les réseaux ZigBee*, Thèse doctorale en Réseaux et télécommunications, Univ. Toulouse le Mirail - Toulouse II, 2008,  
[https://tel.archives-ouvertes.fr/tel-00324196/PDF/these\\_Francomme\\_HAL.pdf](https://tel.archives-ouvertes.fr/tel-00324196/PDF/these_Francomme_HAL.pdf)

**f) Prezentări, workshop-uri, seminarii științifice**

1. **D. Vilcu**, *Informatica în jocurile de șah și GO*, Annual Conference on Applied Mathematics and Informatics – CAMAI, nov. 2017, Bucharest, Romania, <http://camai.spiruharet.ro/>
2. M. Ghica, R. Ioan, **D. Vilcu**, *Neural Networks in R for Pattern Recognition*, Annual Conference on Applied Mathematics and Informatics – CAMAI, nov. 2017, Bucharest, Romania, <http://camai.spiruharet.ro/>
3. **D. Vilcu**, *Essential programming tools for the mobile Web*, Hour of Code, Fac. Matematică și Informatică, Univ. Spiru Haret, dec. 2015
4. **D. Vilcu**, *Building Apps for Person Care*, Annual Conference on Applied Mathematics and Informatics – CAMAI, iun. 2015, Bucharest, Romania, <http://camai.spiruharet.ro/>
5. **D. Vilcu**, *Introducere în programarea aplicațiilor mobile. MIT ApplInventor*, Cercul de Informatică, Fac. Matematică și Informatică, Univ. Spiru Haret, nov. 2014
6. **D. Vilcu**, *Istoria aplicațiilor mobile*, Seminar ISMA - Istoria Statisticii și Matematicii Aplicate, Fac. Matematică și Informatică, Univ. Spiru Haret, nov. 2014

7. **D. Vîlcu**, *Being mobile in bachelor computer science curricula*, Annual Conference on Applied Mathematics and Informatics – CAMAI, iun. 2014, Bucharest, Romania, <http://camai.spiruharet.ro/>
8. **D. Vîlcu**, *Seminar de criptanaliză – Alan Turing*, prelegere introductivă, Fac. Matematică și Informatică, Univ. Spiru Haret, apr. 2014
9. **D. Vîlcu**, *CPU Consumption, from Real-Time Embedded Systems to Mobile Devices*, Annual Conference on Applied Mathematics and Informatics – CAMAI, 7-9 iun. 2013, Bucharest, Romania, <http://camai.spiruharet.ro/>
10. **D. Vîlcu**, *Centenarul Alan Turing*, Seminar ISMA - Istoria Statisticii și Matematicii Aplicate, Fac. Matematică și Informatică, Univ. Spiru Haret, nov. 2012
11. G. Mercier, **D. Vîlcu**, *Predictive Control Based on CMAC Networks for Color Video Transmission in a Wireless LAN*, CAIM 2001, The 9th Intl. Conf. on Appl. and Indust. Math., oct. 2001, Pitești, România
12. **D. Vîlcu**, *Parallel Architectures for Features Extraction Using the RLS Algorithm*, PARADIS 97, Summer School on Parallel and Distributed Systems, Distributed Computing Tempus Project S-JEP 07101, iul. 1997, Mangalia, Romania
13. **D. Vîlcu**, *Modelarea transferului informației la nivel sinaptic*, 1<sup>st</sup> Nat. Conf. Cogn. Sci., mai 1995, Ilieni, Romania